

Notice of References Cited	Application/Control No. 09/836,541	Applicant(s)/Patent Under Reexamination KINTER ET AL.	
	Examiner Daniel Pan	Art Unit 2183	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,740,392	04-1998	Brennan, Bob	712/210
*	B	US-6,442,674	08-2002	Lee et al.	712/205
*	C	US-5,539,680	07-1996	Palnitkar et al.	703/13
*	D	US-2002/0107678	08-2002	Wu et al.	703/14
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	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Morris Chang et al., Designning A Java Microprocessor Core Using FPGA Technology, Pages 13,17, 1998, IEEE.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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